

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	jalal-hallak.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:12
L2	4	hallak-jalal.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:15
L3	25	schweigert-harald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:36
L4	4	("4914542"   "4937697"   "5818670"   "6157089").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/17 12:20
L5	0	("7072160").URPN.	USPAT	OR	ON	2007/03/17 12:22
L6	0	("7072160").URPN.	USPAT	OR	ON	2007/03/17 12:22
L7	100	gate near3 diode near4 (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:50
L8	7	7 and ((short adj circuit) (over adj load) (over adj current) (excess\$3 adj current) (exceed\$3 adj current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:56
L9	330	gate near4 (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system)) with diode	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:47
L10	40	9 and ((short adj circuit) (over adj load) (over adj current) (excess\$3 adj current) (exceed\$3 adj current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:49
L11	1347	gate with diode with (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:55
L12	176	11 and ((short adj circuit) (over adj load) (over adj current) (excess\$3 adj current) (exceed\$3 adj current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 12:56
L13	140	("4631625").URPN.	USPAT	OR	ON	2007/03/17 13:36
L14	1540	361/93.1,93.7,94.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:49

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L15	790	361/93.7,94.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:49
L16	0	15 and gate near3 diode near4 (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:50
L17	2	15 and gate with diode with (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:52
L18	3	361/58.ccls. and gate with diode with (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:53
L19	2	361/8-10,13.ccls. and gate with diode with (cpu computer processor microcomputer microprocessor (control adj unit) (control adj system) microcontroller (monitor\$3 adj unit) (monitor\$3 adj system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/03/17 13:53
L20	0	gate with monitoring adj unit with zener adj diode with (auxiliary adj3 switch) with trigg\$3.clm.	US-PGPUB	OR	ON	2007/03/17 13:55
L21	1	gate with (monitoring adj unit) with (zener adj diode) with switch.clm.	US-PGPUB	OR	ON	2007/03/17 13:55